

Sir:



Transmitted herewith for filing is the Patent Application of:

Inventor: CHI-CHUN CHEN, TZU-LIANG LEE, SHIH-CHANG CHEN

METHOD OF FORMING DUAL GATE INSULATOR LAYERS FOR CMOS APPLICATIONS



Enclosed are:

2 sheets of drawing(s) - formal.  
 An assignment of the invention to Taiwan Semiconductor Manufacturing Co.  
 An associate power of attorney  Applicant claims small entity status  
 Request & Certification under 35 USC 122(b)(2)(b)(i)

The filing fee has been calculated as shown below:

(Col. 1)	(Col. 2)	OTHER THAN A SMALL ENTITY
FOR:	NO. FILED	NO. EXTRA
BASIC FEE	X	X
TOTAL CLAIMS	33 -20=	13
INDEP CLAIMS	2 -3=	0
		SUB TOTAL
		\$ 984.
		ASSIGNMENT
		\$40.
		TOTAL
		\$ 1,024.

Please charge my Deposit Account No. 19-0033 in the amount of \$ 1,024. A duplicate copy of this sheet is enclosed.  
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 Any additional filing fees required under 37 CFR §1.16.  
 Any patent application processing fees under 37 CFR §1.17.

Respectfully submitted,  
  
STEPHEN B. ACKERMAN, REG. NO. 37,761

EXPRESS MAIL CERTIFICATE

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I hereby certify that this correspondence is being deposited with the United States Postal Service as Express Mail in an envelope addressed to Assistant Commissioner of Patents, Alexandria, VA, 22313-1450. Applicant and/or Attorney requests the date of deposit as the Filing Date.

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